

IN THE SPECIFICATION

Please amend paragraph 0016 of the application as follows.

[0016] ~~Figure 1B is diagram~~ Figures 1B and 1C are diagrams of an alternative power ~~deteeter~~ detectors that may be used in accordance with the present invention;

Please amend paragraph 0050 of the application as follows.

[0050] While a particular summing amplifier 23 is provided in the exemplary circuit of Figure 1B, any other known circuits capable of summing the outputs of the plural power detectors 2 may be used. In addition, instead of using a summing circuit, other circuits may be coupled to the output of the diodes 16, to create a different type of signal reflective of the detected power at the plural interior nodes. For instance, referring to Figure 1C, a differential signal may be produced. That is, in place of summing amplifier 23, a differential amplifier 23 may be used that determines a difference between the signal outputs of the respective power detectors 2, and outputs a differential signal that reflects the output power.